

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Chin-Kai SUN, et al. )  
 )  
Serial No.: Not yet assigned ) Group: Not yet assigned  
 ) Examiner: Not yet assigned  
Filed: Concurrently herewith )  
 ) Our Ref: B-5373 621704-5  
For: "MECHANISM FOR INTERCONNECTING )  
TWO PARALLEL CIRCUIT BOARDS" ) Date: February 13, 2004

CLAIM TO PRIORITY UNDER 35 U.S.C. 119

MAIL STOP PATENT APPLICATION  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

[X] Applicants hereby make a right of priority claim under 35  
U.S.C. 119 for the benefit of the filing date(s) of the  
following corresponding foreign application(s):

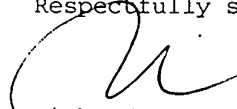
<u>COUNTRY</u>	<u>FILING DATE</u>	<u>SERIAL NUMBER</u>
TAIWAN, R.O.C.	14 February 2003	092202413

[ ] A certified copy of each of the above-noted patent  
applications was filed with the Parent Application  
No. \_\_\_\_\_.

[ ] To support applicant's claim, a certified copy of the above-  
identified foreign patent application is enclosed herewith.

[X] The priority document will be forwarded to the Patent Office  
when required or prior to issuance.

Respectfully submitted,



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